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Featured The Cyclone III FPGA Starter Kit features the EP3C25F324 device (U1) in a 324-pin

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FineLine BGA (FBGA) package. Table 2-2 lists Cyclone III Device device features. Page 14: Clocking Circuitry Table 2-3: The Cyclone III EP3C25F324 only supports one I/O standard in an I/O bank.

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Cyclone III FPGA Starter Board Reference Manual April 2012 Clocking Circuitry Table 2-3 lists the Cyclone III EP3C25F324 device pin count. You can configure the Cyclone III device via the on-board USB-Blaster™ or through the JTAG interface using an external programming cable (sold separately). f For additional information about Altera ...

Cyclone III FPGA Starter Board Reference Manual

101 Innovation Drive San Jose, CA 95134 www.altera.com Cyclone III 3C120 Development Board Reference Manual Document Version: 1.4 Document Date: March 2009

Cyclone III Development Board Reference Manual

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A.O. SMITH - A4-manuals - CYCLONE

Manuel-Utilisateur COMPAIR Cyclon 111 Le Mode d'emploi (ou Manuel utilisateur ou Notice d'utilisation) détaille les modes de fonctionnement de l'équipement : précautions avant utilisation, description des commandes et boutons, modes opératoires, les actions à mener en cas de dysfonctionnement, les conditions de garantie...

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Cyclone III FPGA Development Kit - Intel

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